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C. Healion
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Applicant : Jafar Savoj
Application No. : 09/782,687
Filed : February 12, 2001
Title : LINEAR HALF RATE PHASE DETECTOR AND CLOCK AND DATA
RECOVERY CIRCUIT
Confirmation No. 4983
Date of Notice
of Allowance : September 24, 2004
Grp./Div. : 2633
Examiner : Nathan M. Curs
Docket No. : 50991/RJP/B600

TRANSMITTAL OF DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Post Office Box 7068
Pasadena, CA 91109-7068
November 29, 2004

Commissioner:

Enclosed are 13 sheets of drawings for this application. They are submitted as formal drawings to replace the corresponding informal drawings as originally filed.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

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